

IN THE CLAIMS (As Amended to Incorporate the Article 34 Amendments):

Please amend claims 1-8, as follows:

1. (Amended) A method for manufacturing a printed circuit board [characterized by]
comprising the steps of:

coating a lower surface and an upper surface of an insulative substrate respectively with a lower surface metal foil and an upper surface metal foil, the thickness of which is less than that of the lower surface metal foil;

forming an opening in the upper surface metal foil at a location corresponding to a blind via hole formation portion of the insulative substrate;

forming a blind via hole, the bottom of which is the lower surface metal foil, by emitting a laser against the blind via hole formation portion through the opening;

applying a conductor to the blind via hole; and

forming an upper surface pattern and a lower surface pattern by respectively etching the upper surface metal foil and the lower surface metal foil.

2. (Amended) A method for manufacturing a printed circuit board [characterized by]
comprising the steps of:

coating a lower surface and an upper surface of an insulative substrate respectively with a lower surface metal foil and an upper surface metal foil, the thickness of which is less than that of the lower surface metal foil;

forming an upper surface pattern and a lower surface pattern by respectively etching the upper surface metal foil and the lower surface metal foil, wherein the upper surface pattern has an opening exposing the upper surface of the insulative substrate at a location corresponding to a

blind via hole formation portion, and the lower surface pattern covers the lower surface of the insulative substrate at a location corresponding to the blind via hole formation portion;

forming a blind via hole, the bottom of which is the lower surface pattern, by emitting a laser against the insulative substrate through the opening; and
applying a conductor to the blind via hole.

3. (Amended) The printed circuit board manufacturing method according to claim 1 [or 2], [characterized in that] wherein the upper surface and lower surface metal foil coating step includes a step of coating the upper surface and the lower surface, respectively, with an upper surface metal foil and a lower surface metal foil that have the same thickness, and a step for etching the upper surface metal foil.

4. (Amended) The printed circuit board manufacturing method according to claim 1 [or 2], [characterized in that] wherein the upper surface and lower surface metal foil coating step includes a step for coating the upper surface and the lower surface, respectively, with an upper surface metal foil and a lower surface metal foil that have the same thickness, and a step for further coating the lower surface metal foil with a metal plating film.

5. (Amended) The printed circuit board manufacturing method according to claim 1 [or 2], [characterized in that] wherein the upper surface and lower surface metal foil coating step includes a step for coating the upper surface and the lower surface, respectively, with an upper surface metal foil and a lower surface metal foil that have the same thickness, and a step for

performing a sandblast treatment to the upper surface metal foil so that the thickness of the upper surface metal foil becomes less than that of the lower surface metal foil.

6. (Amended) The printed circuit board manufacturing method according to claim 1 [or 2], [characterized in that] wherein the thickness of the upper surface pattern is 2 to 12 μm .

7. (Amended) The printed circuit board manufacturing method according to claim 1 [or 2], [characterized in that] wherein the thickness of the lower surface pattern is 15 to 25 μm .

8. (Amended) A printed circuit board [characterized by] comprising:

an insulative substrate;

an upper surface pattern and a lower surface pattern provided, respectively, on an upper surface and a lower surface of the insulative substrate; and

a blind via hole for electrically connecting the upper surface pattern and the lower surface pattern, wherein an upper portion of the blind via hole is opened and a bottom of the blind via hole is covered by the lower surface pattern, the thickness of the upper surface pattern being less than that of the lower surface pattern.

Please add claims 9-13, as follows:

9. (New) The printed circuit board manufacturing method according to claim 2, wherein the upper surface and lower surface metal foil coating step includes a step of coating the upper surface and the lower surface, respectively, with an upper surface metal foil and a lower surface metal foil that have the same thickness, and a step for etching the upper surface metal foil.

10. (New) The printed circuit board manufacturing method according to claim 2, wherein the upper surface and lower surface metal foil coating step includes a step for coating the upper surface and the lower surface, respectively, with an upper surface metal foil and a lower surface metal foil that have the same thickness, and a step for further coating the lower surface metal foil with a metal plating film.

11. (New) The printed circuit board manufacturing method according to claim 2, wherein the upper surface and lower surface metal foil coating step includes a step for coating the upper surface and the lower surface, respectively, with an upper surface metal foil and a lower surface metal foil that have the same thickness, and a step for performing a sandblast treatment to the upper surface metal foil so that the thickness of the upper surface metal foil becomes less than that of the lower surface metal foil.

12. (New) The printed circuit board manufacturing method according to claim 2, wherein the thickness of the upper surface pattern is 2 to 12 μ m.

13. (New) The printed circuit board manufacturing method according to claim 2, wherein the thickness of the lower surface pattern is 15 to 25 μ m.

IN THE DRAWINGS:

Amend Figs. 4(a), 4(b) and 4(x) to include the legend -(Prior Art)--, as indicated in red on the attached copies of Figs. 4(a), 4(b) and 4(x).